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PATENT
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Noriyuki SARUHASHI, et al.

Serial No: 09/871,596

Filed: May 30, 2001

For: PHYSICAL LAYER DEVICE TESTING
METHOD, PHYSICAL LAYER DEVICE
WITH TEST CIRCUITS, AND
TRANSMITTING/RECEIVING CIRCUIT
WITH TEST CIRCUITS

Art Unit: 2133

Examiner: Joseph D. Torres

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Technology Center 2100

Dear Sir:

In accordance with 37 C.F.R. 1.136, Applicant respectfully petitions the Commissioner for a one-month extension of time extending to July 26, 2004 (July 24, 2004 being a Saturday), the period for response to the final Office Action dated March 24, 2004. A check for \$110 is enclosed. The responsive paper(s) are attached.

Please charge any insufficiency or credit any overpayment to Deposit Account No. 50-1314. A copy of this petition is enclosed.

Respectfully submitted,

HOGAN & HARTSON L.L.P.

By: *Anthony J. Orler*

Anthony J. Orler
Registration No. 41,232
Attorney for Applicant(s)

08/02/2004 HALI11 0000040 09871596

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110.00 OP

Date: July 26, 2004

500 South Grand Avenue, Suite 1900
Los Angeles, California 90071
Phone: 213-337-6700
Fax: 213-337-6701